CHARACTERISTICS	SPECIFICATIONS	ADDITIONAL INFORMATION
★ Acquisition Time (for 20.4 div) 60s/div...1 µs/div	20.5 x time/div + 140 ms	Excluding delay time. Delay time is the selected trigger delay.
500 ns/div...10 ns/div	20.5 x time/div + 120 ms	Excluding delay time. In Quasi-Random Mode, the acquisition time depends on triggers.
★ Sources	Channel ± A Channel ± B mV Input	Inverted Channel A only for software V4.01 and higher versions.
★ Acquisition Modes	1 Channel Only 2 Channels	CHAN A, CHAN B Chopped Mode from 60s/div...50 µs/div. Alternating Mode from 20 µs/div...10 ns/div.

2.3   CHANNELS A & B

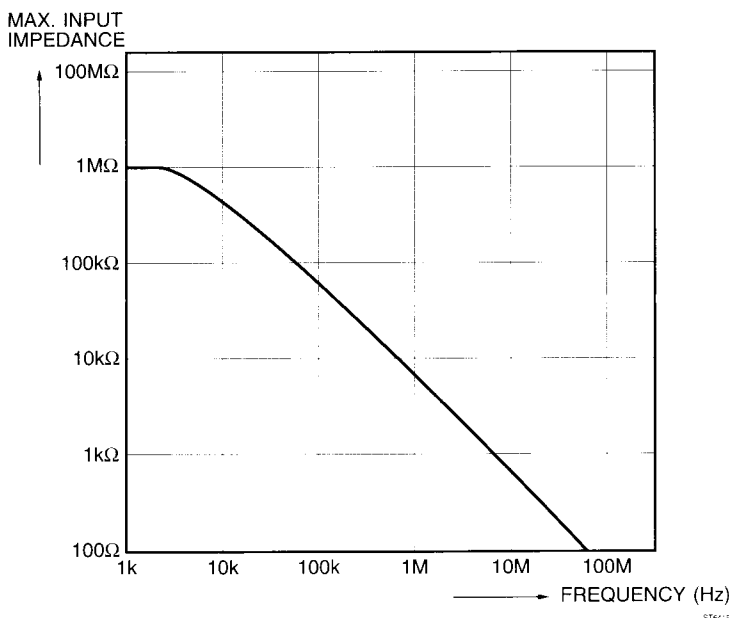|		
★ Signal Inputs	Isolated BNC	**Signal input BNC commons are connected together.**
Common Input	Black Safety Banana Jack	Part of External Trigger Input.
★ Input Impedance		Frequency dependent, see Figure 2.1.
R parallel	1 MΩ ± 1%	For DC coupled input. For AC coupled input or GND, add 22 nF in series with R and C parallel.
C parallel	25 pF	

Figure 2.1   Max. Input Impedance Versus Frequency